**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claims 1-20 (Canceled)

21. (New) An electronic apparatus provided with a first memory device and a second memory device which can be removably stored in a relatively flat space, the first and second memory devices including input terminals and output terminals, the electronic apparatus comprising:

a board;

a controller for controlling the memory devices, which is mounted on the board;

a resistance for terminating an electric signal, which is mounted on the board;

a first socket for supporting the first memory device, mounted on the board, and having a first terminal electrically connected to the input terminal of the first memory device and a second terminal electrically connected to the output terminal of the first memory device when the first memory device is set on the first socket;

a second socket for supporting the second memory device, mounted on the board, having a third terminal electrically connected to the input terminal of the second memory device and a fourth terminal electrically connected to the output terminal of the second memory device when the second memory device is set on the second socket; and

a pattern wiring arranged on the board, the pattern wiring including a first wiring portion wired between the controller and the first terminal, a second wiring portion wired between the second terminal and the third terminal, and a third wiring portion between the fourth terminal and the resistance, wherein the pattern wiring is located in a preset position other than a position in which the first and second sockets are located.

22. (New) The electronic apparatus according to claim 21, wherein the board has a multi-layered structure including a surface layer, and the pattern wiring is formed in the surface layer.

23. (New) The electronic apparatus according to claim 21, further comprising a clock generator for supplying a clock signal to the first and second memory devices, the clock generator being mounted on the board.

24. (New) The electronic apparatus according to claim 21, wherein the board has a first surface and a second surface opposed to the first surface, and wherein the controller, the resistance, and the first and second sockets are arranged on the first surface of the board.

25. (New) The electronic apparatus according to claim 21, wherein the first and second sockets are so arranged on the first surface so that the second terminal of the first socket faces the third terminal of the second socket.

26. (New) An electronic apparatus in which a first memory device, a second memory device, and third memory device can be removably stored, the first, second and third memory devices including input terminals and output terminals, the electronic apparatus comprising:

a board having a first surface and a second surface opposed to the first surface;

a controller for controlling the memory devices, which is mounted on the first surface of the board;

a resistance for terminating an electric signal, which is mounted on the first surface of the board;

a first socket for supporting the first memory device, mounted on the first surface of the board, having a first terminal electrically connected to the input terminal of the first memory device and a second terminal electrically connected to the output terminal of the first memory device when the first memory device is set on the first socket;

a second socket for supporting the second memory device, mounted on the second surface of the board, having a third terminal electrically connected to the input terminal of the first memory device and a fourth terminal electrically connected to the output terminal of the second memory device when the second memory device is set on the second socket;

a third socket for supporting the third memory device, mounted on the first surface of the board, having a fifth terminal electrically connected to the input terminal of the first memory device and a sixth terminal electrically connected to the output terminal of the second memory device when the third memory device is set on the second socket; and

a first pattern wiring including a first wiring portion wired between the controller and the first terminal on the first surface, a second wiring portion wired between the second terminal and the third terminal, which is extended through the board, a third wiring portion between the fourth terminal and the fifth terminal on the second surface, a fourth wiring between the fifth terminal and the sixth terminal, which is extended through the board, and a fifth wiring portion between the sixth terminal and the resistance on the first surface.

27. (New) The electronic apparatus according to claim 26, wherein the board has a multi-layered structure including surface layers on the first and second surfaces, respectively, the first and fifth wiring portions are formed in the surface layer on the first surface, the third wiring portion is formed in the surface layer on second surface, and the second and fourth wiring portions are extended through the surface layers of the board.

28. (New) The electronic apparatus according to claim 26, further comprising a clock generator for supplying a clock signal to the first and second memory devices, the clock generator being mounted on the first surface of the board.

29. (New) A memory device comprising:

first, second and third storage means for storing information, each provided with input and output terminals;

means for controlling the first, second and third storage means, which is provided with an output terminal, the input terminal of the first storage means facing the output terminal of the means for controlling;

means having a resistance, for terminating an electric signal, which is provided with an input terminal, the output terminal of the third storage means facing the input terminal of the means for terminating; and

pattern wirings for electrically connecting the output terminal of the means for controlling to the input terminal of the first storage means, the output terminal of the first storage means to the input terminal of the second storage means, the output terminal of the second storage means to the input terminal of the third storage means, and the output terminal of the third storage means to the input terminal of the means for terminating to transmit the electric signal;

a board having a first board surface and a second board surface opposed to the first board surface, the first and third storage means, the means for controlling and the means for terminating being arranged on the first board surface, the second storage means being arranged on the second board surface, the pattern wiring being located in a preset position on the insulating portion of the board other than a position in which the storage means are located.

30. (New) The memory device according to claim 29, wherein the board is composed of multiple layers.

31. (New) The memory device according to claim 29, wherein the electric signal is transmitted from the means for controlling to the means for terminating.